

Solving the AI Crisis



Memories for a connected world

AI and Datacenters: Energy Demand Exploding



Data centers use 5% of US electricity

- To double 2-3x next 3-4 years

Meta's Hyperion:

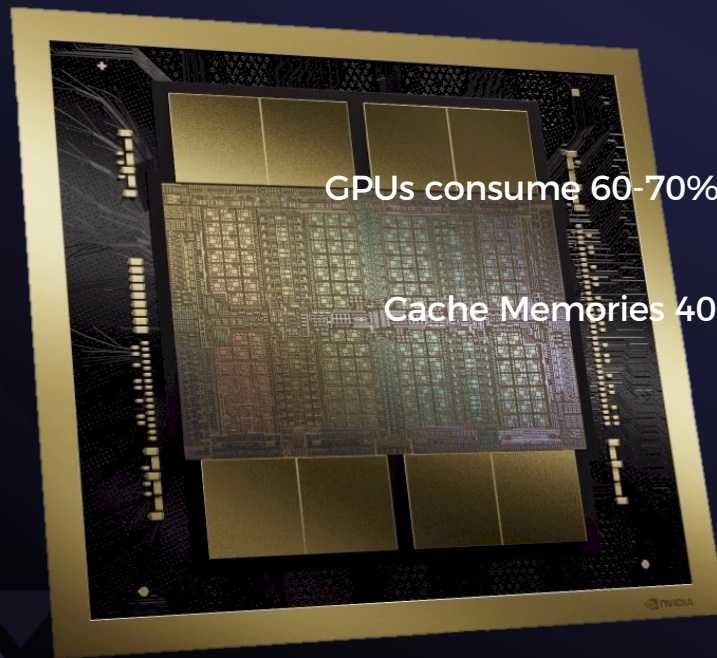
- 1.5 Million GPU's
- 5GW \approx 5 nuclear reactors
- \approx \$2B and 20M tons CO₂ / year



The Problem: On-chip Memories

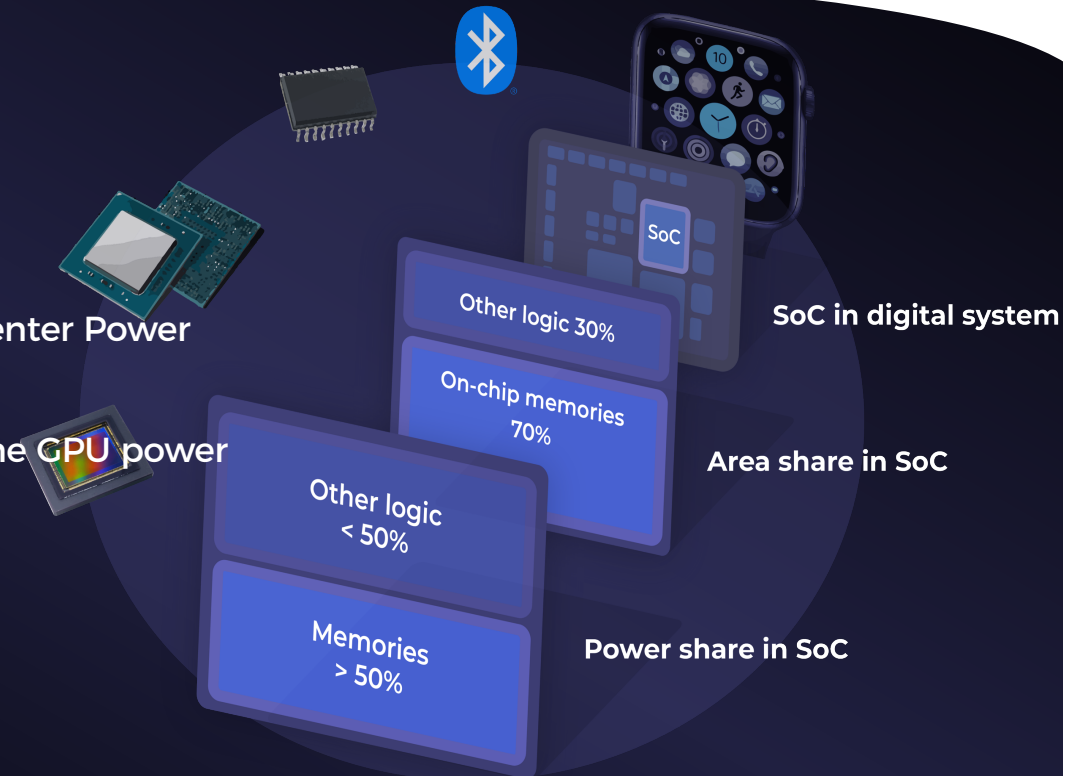


NVIDIA's Blackwell GPU



GPUs consume 60-70% of Datacenter Power

Cache Memories 40-50% of the GPU power



The Solution:

Xenergetic's Low Power Design

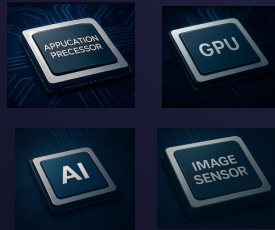


Patented Memory IP

- 50% lower power
- Scalable business model - \$10B potential
- Bosch as first customer
- Ready to scale with online compilers

Also applicable to application processors & ultra-low power devices (e.g. MEMS sensors)

Traction:



- First Customers – In Mass Production
- Ongoing discussions with Semiconductor Foundries and Chip Designers
- Online Compiler – Ready to Scale
- Protected IP – 9 Patent Families

Team:



- +10 years of research at Lund University
- Comprehensive expertise across memory architecture, physical design, verification, software, and sales.



Babak Mohammadi, PhD
CTO, COB & Co-Founder

- Expert in low-power and memory design
- 15 y of experience in RD & industry



Anders Berglund
CEO & Co-Founder

- Entrepreneur
- Strategic Advisor
- 20 years of deep-tech start-ups



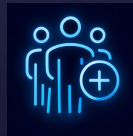
Cajsa Wramdemark,
CFO

- CFO experience from several publicly listed companies

The Ask & Why Now:



We are raising €3-10M to



Expand the team



Accelerate sales



Capture AI/HPC

Secure €200 M in annual “lifetime” sales in 2029

Xenergic – Cutting Power in Half Capturing AI/HPC

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